

MAY 2011

**P/ID 17462/RCM/  
PCAN**

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Time : Three hours

Maximum : 75 marks

PART A — (5 × 5 = 25 marks)

Answer ALL questions.

1. (a) Explain Instruction formats.

Or

(b) Give a note on Data manipulation instructions.

2. (a) Write about Vector Processing.

Or

(b) Explain SIMD Array processor.

3. (a) Write a note on BCD Adder.

Or

(b) Explain Division algorithms.

4. (a) Write about CPU-IOP Communication.

Or

(b) Give a note on Asynchronous communication interface.

5. (a) Give a note on Memory protection.

Or

- (b) Describe Associative memory page table.

PART B — (5 × 10 = 50 marks)

Answer any FIVE questions.

6. Describe Stack organization.
7. Explain Instruction Pipelining.
8. Summarize Floating-point arithmetic operations.
9. Describe Direct Memory Access (DMA).
10. Illustrate Auxiliary memory.
11. Explain Addressing Modes.
12. Give a detailed note on Serial Communication.
13. Explain Associative memory.
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