

MAY 2013

P/ID 17412/RBN

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Time : Three hours

Maximum : 75 marks

PART A — (5 × 5 = 25 marks)

Answer ALL questions.

All questions carry equal marks.

1. (a) Explain three state bus buffers.

Or

(b) Write short notes on binary adder-subtractor.

2. (a) Write short notes on addressing modes.

Or

(b) Explain pipelines with example.

3. (a) Explain Booth multiplication algorithm.

Or

(b) What is decimal arithmetic unit? Explain BCD (Binary Coded Decimal) adder.

4. (a) Describe modes of transfer.

Or

(b) Explain DMA controller with block diagram.

5. (a) Write short notes on memory management hardware.

Or

- (b) Describe the characteristics of multiprocessors.

PART B — (5 × 10 = 50 marks)

Answer any FIVE questions.

All questions carry equal marks.

6. Explain in detail about vector processing.
7. Discuss on address sequencing.
8. Describe general register organization with neat diagram.
9. Explain vector processing with example.
10. Describe decimal arithmetic operations.
11. Explain serial communication.
12. What is cache memory? Explain its mapping procedures.
13. Discuss on inter-processor communication and synchronization.