OCTOBER 2012 **P/ID 17462/RCM/ PCAN**

Time : Three hours Maximum : 75 marks

PART A — ($5 \times 5 = 25$ marks).

Answer ALL questions.

1. (a) Explain Instruction formats.

Or

(b) Give a note on Data manipulation instructions.

2. (a) Write about Vector Processing.

Or

(b) Explain RISC pipeline.

3. (a) Write a note on BCD Adder.

Or

(b) Write an algorithm for Booth multiplication algorithm.

4. (a) Explain modes of Transfer.

Or

(b) Give a note on Asynchronous communication interface.

5. (a) Give a note on Memory protection.

Or

(b) Write a note on Segmented-page mapping.

PART B —(5 × 10 = 50 marks)

Answer any FIVE questions.

6. Describe Stack organization.

7. Explain Instruction Pipelining.

8. Explain Multiplication algorithms.

9. Describe Direct Memory Access (DMA).

10. Illustrate Auxiliary memory.

11. Explain Addressing Modes.

12. Give a detailed notes on Serial Communication.

13. Explain Associative memory.

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