

OCTOBER 2011

P/ID 17412/RBN

Time : Three hours

Maximum : 75 marks

PART A — (5 × 5 = 25 marks)

Answer ALL questions.

1. (a) Write short notes on Register transfer.

Or

(b) List some applications of logic micro operations.

2. (a) Explain the RISC characteristics.

Or

(b) Explain array processors.

3. (a) Define any one decimal arithmetic operation with example.

Or

(b) Explain hardware multiply algorithm.

4. (a) Explain the various mode of data transmission.

Or

- (b) Explain isolated Vs memory mapped I/O.

5. (a) Differentiate RAM and ROM chips.

Or

- (b) Differentiate tightly coupled and loosely coupled microprocessor.

PART B — (5 × 10 = 50 marks)

Answer any FIVE questions.

6. Explain the design of control unit.
7. Explain with example, pipeline processing.
8. Write notes on various interrupt :
- (a) types
- (b) write notes on stack organization.
9. Explain Booth multiplication algorithm.
10. Describe SIMD array processor.

11. Describe DMA controller with block diagram.
 12. (a) Write notes on :
 - (i) Magnetic Disk
 - (ii) Magnetic tape
 - (b) How read write operation takes place in associative memory?
 13. Explain about various mappings in cache memory.
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